

**FIG. 1a**

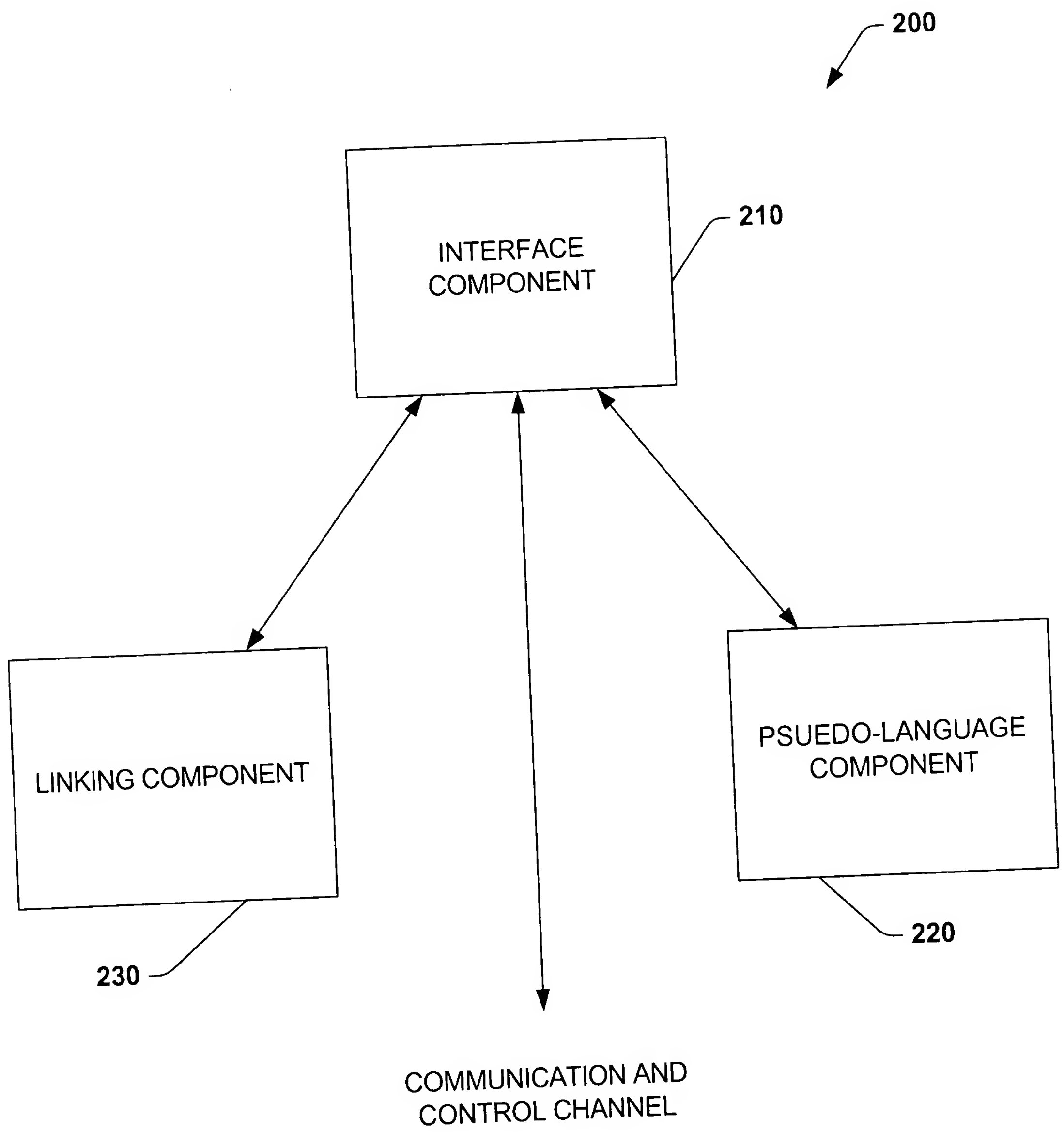
150

| Action | Primitive Operation | Reserved | Size | Register Region |       |        |          |             |             |             |             | Value       | Mask        |
|--------|---------------------|----------|------|-----------------|-------|--------|----------|-------------|-------------|-------------|-------------|-------------|-------------|
|        |                     |          |      | ID              | Width | Offset | Reserved | Address s 0 | Address s 1 | Address s 2 | Address s 3 |             |             |
| 0x09   | 0x82                | 0        | 0x04 | 0x02            | 0x04  | 0x00   | 0x00     | 0x0000      | 0x0007      | 0x0003      | 0x006c      | 0x000000400 | 0x000000400 |

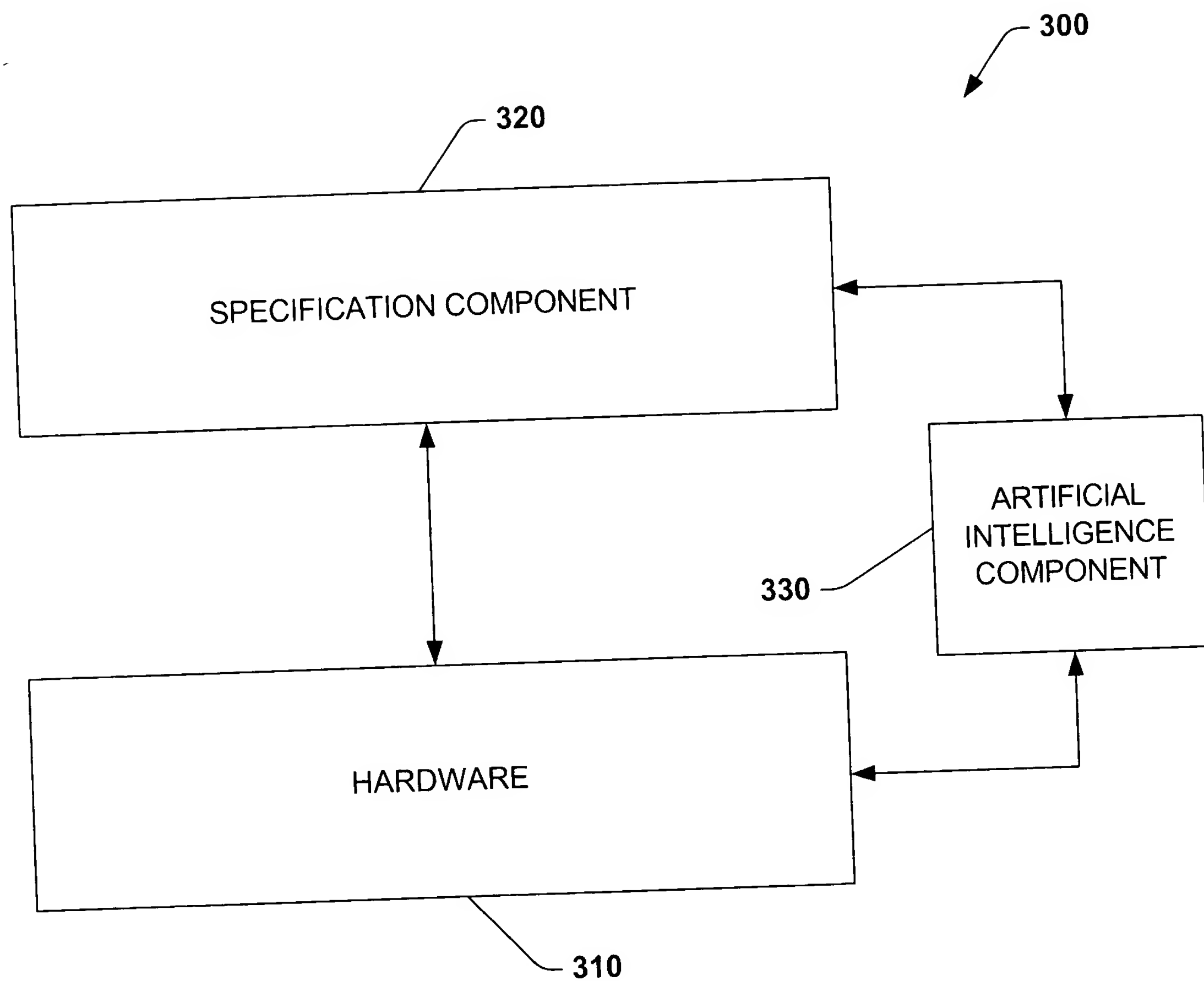
160

| Action | Primitive Operation | Reserved | Size | Register Region |       |        |          |             |             |             |             | Value       | Mask        |
|--------|---------------------|----------|------|-----------------|-------|--------|----------|-------------|-------------|-------------|-------------|-------------|-------------|
|        |                     |          |      | ID              | Width | Offset | Reserved | Address s 0 | Address s 1 | Address s 2 | Address s 3 |             |             |
| 0x0B   | 0x82                | 0        | 0x04 | 0x02            | 0x04  | 0x00   | 0x00     | 0x0000      | 0x0007      | 0x0003      | 0x006c      | 0x000000000 | 0x000000400 |

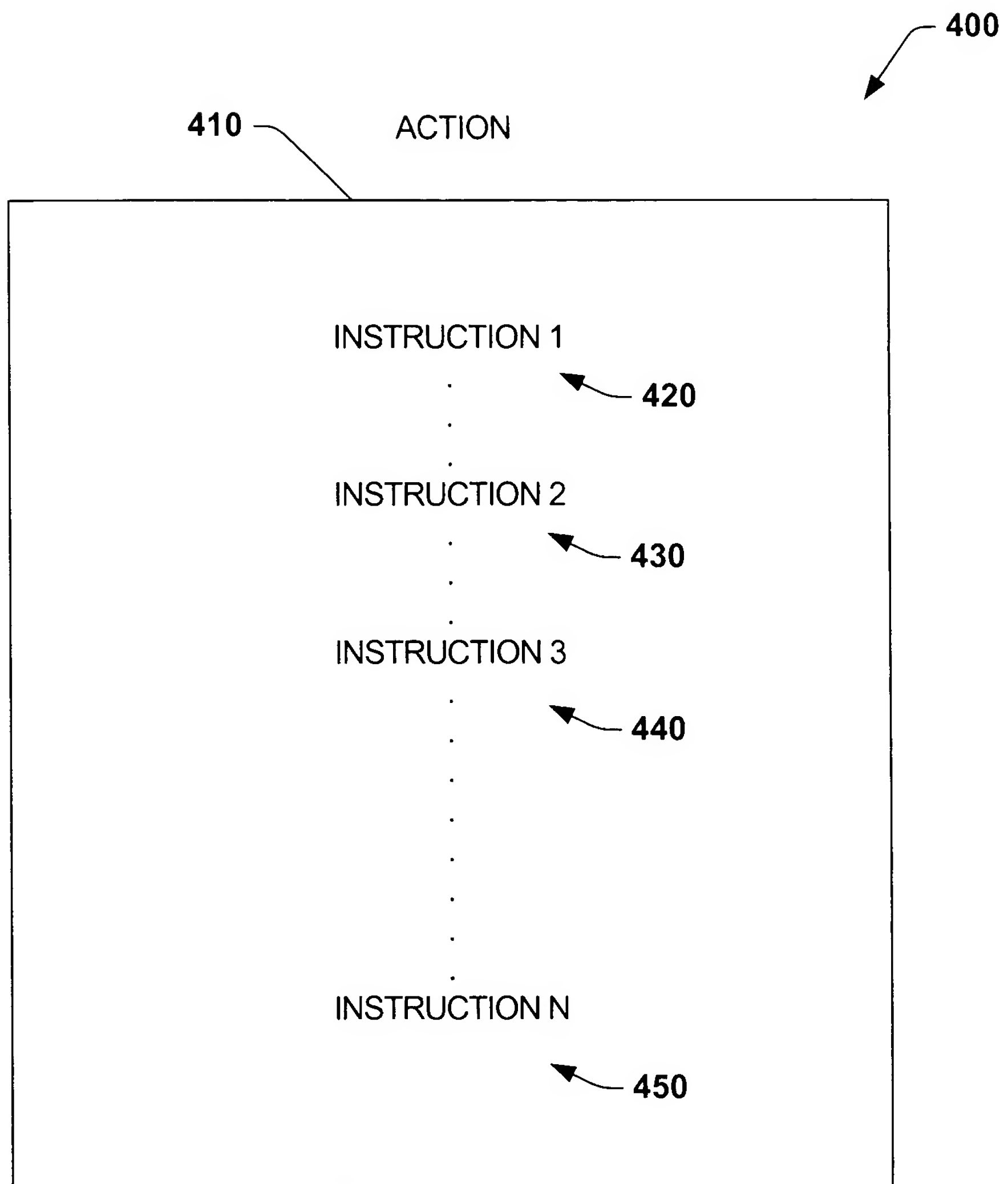
FIG. 1b



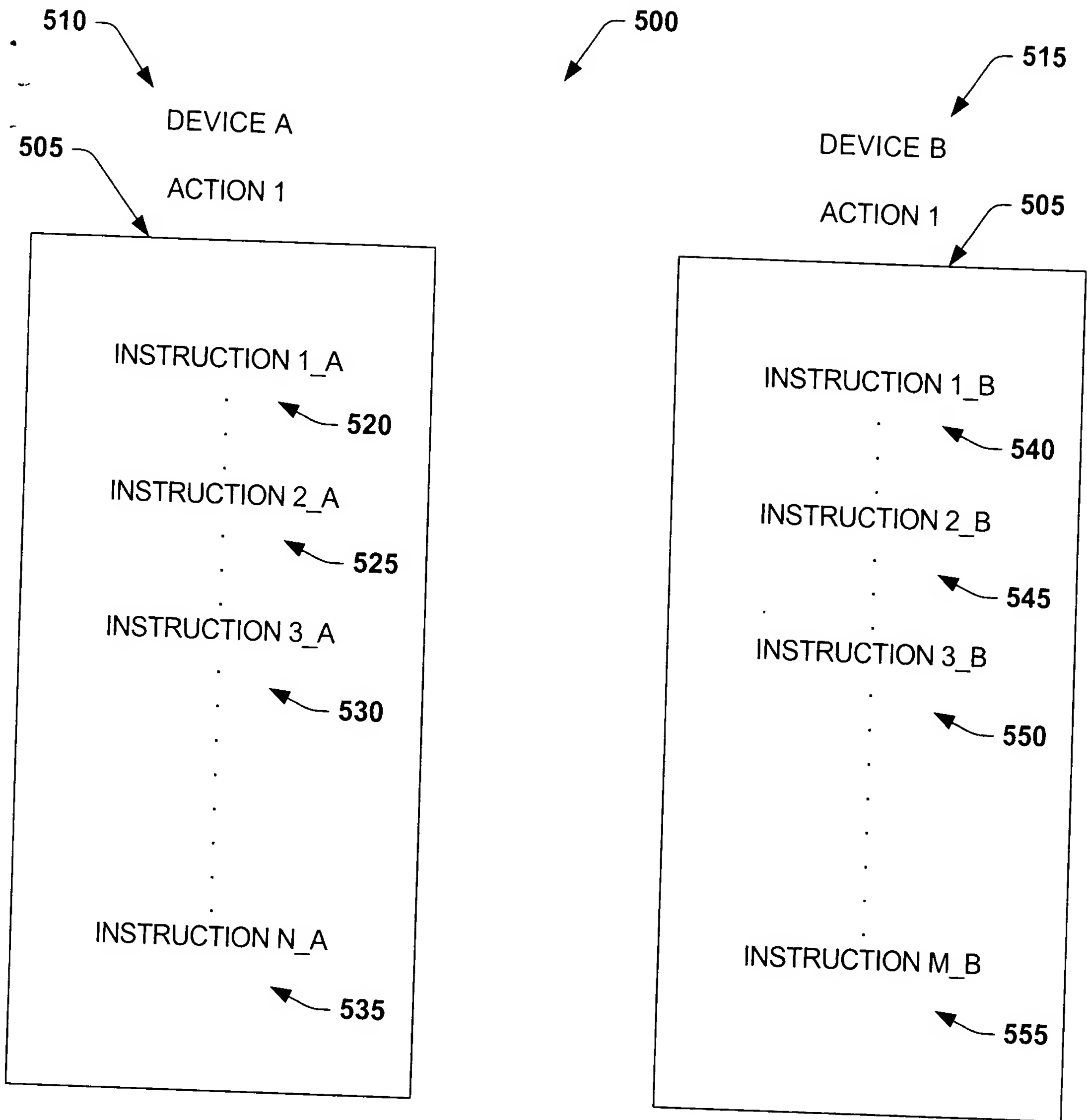
**FIG. 2**



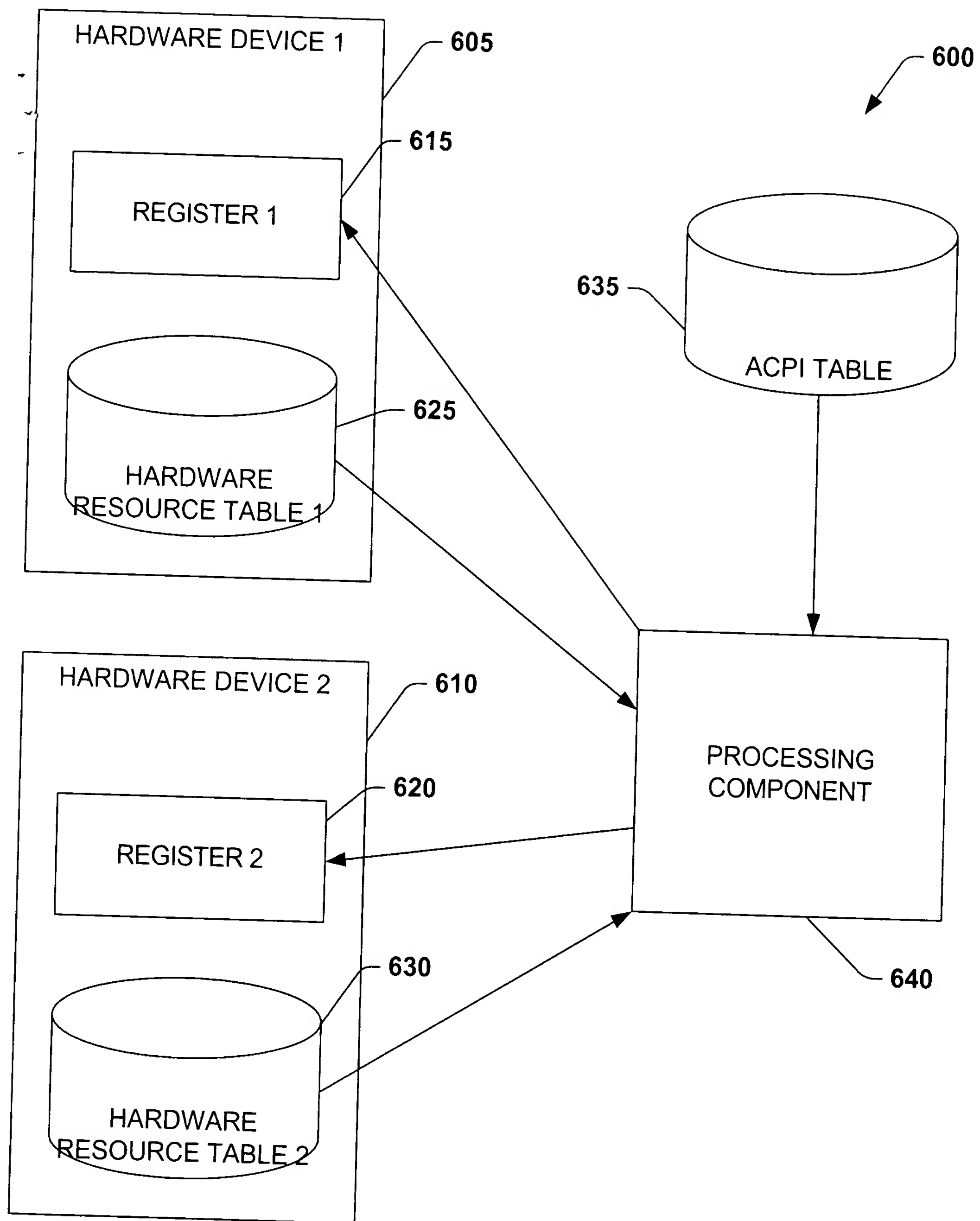
**FIG. 3**



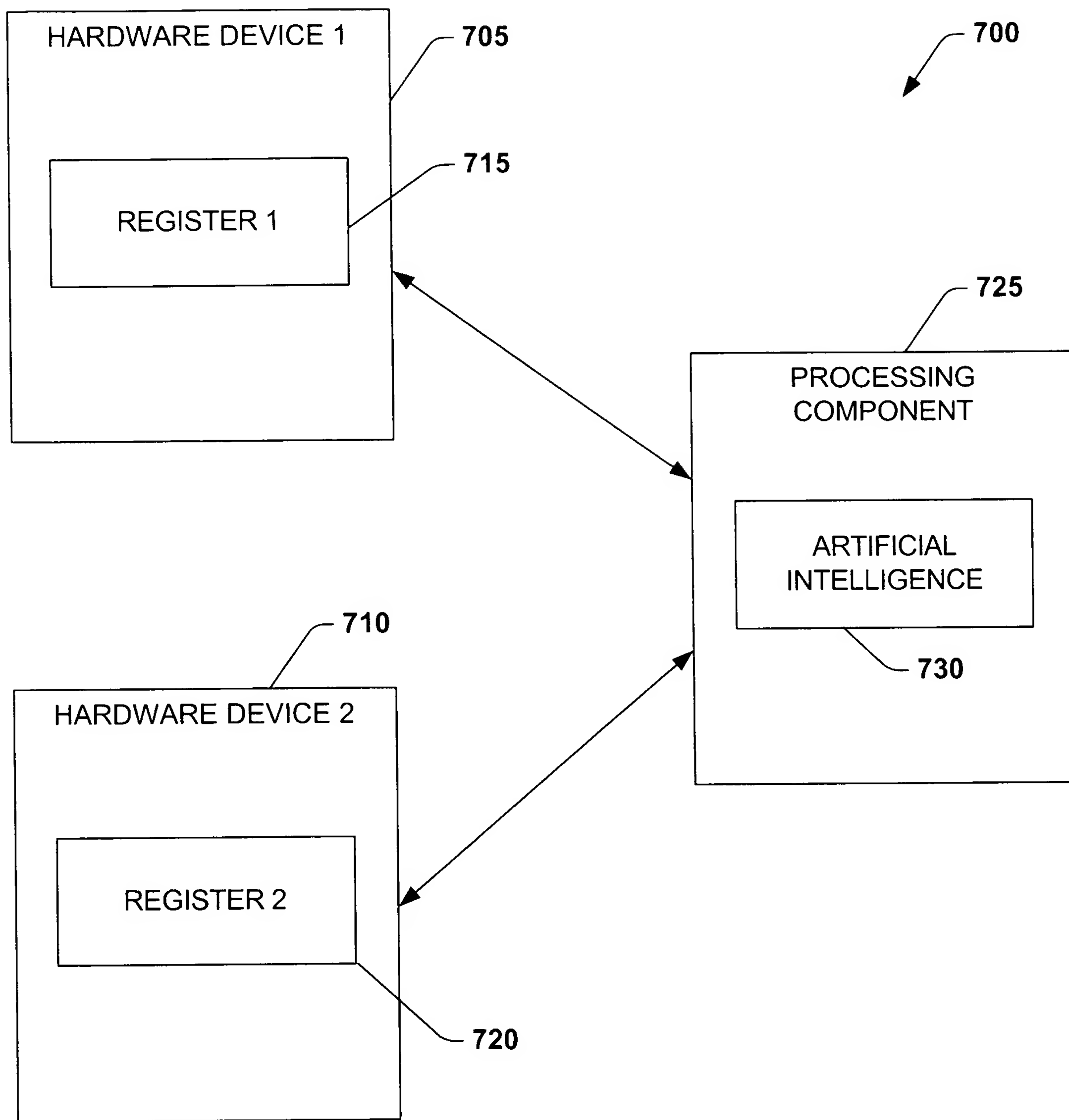
**FIG. 4**



**FIG. 5**



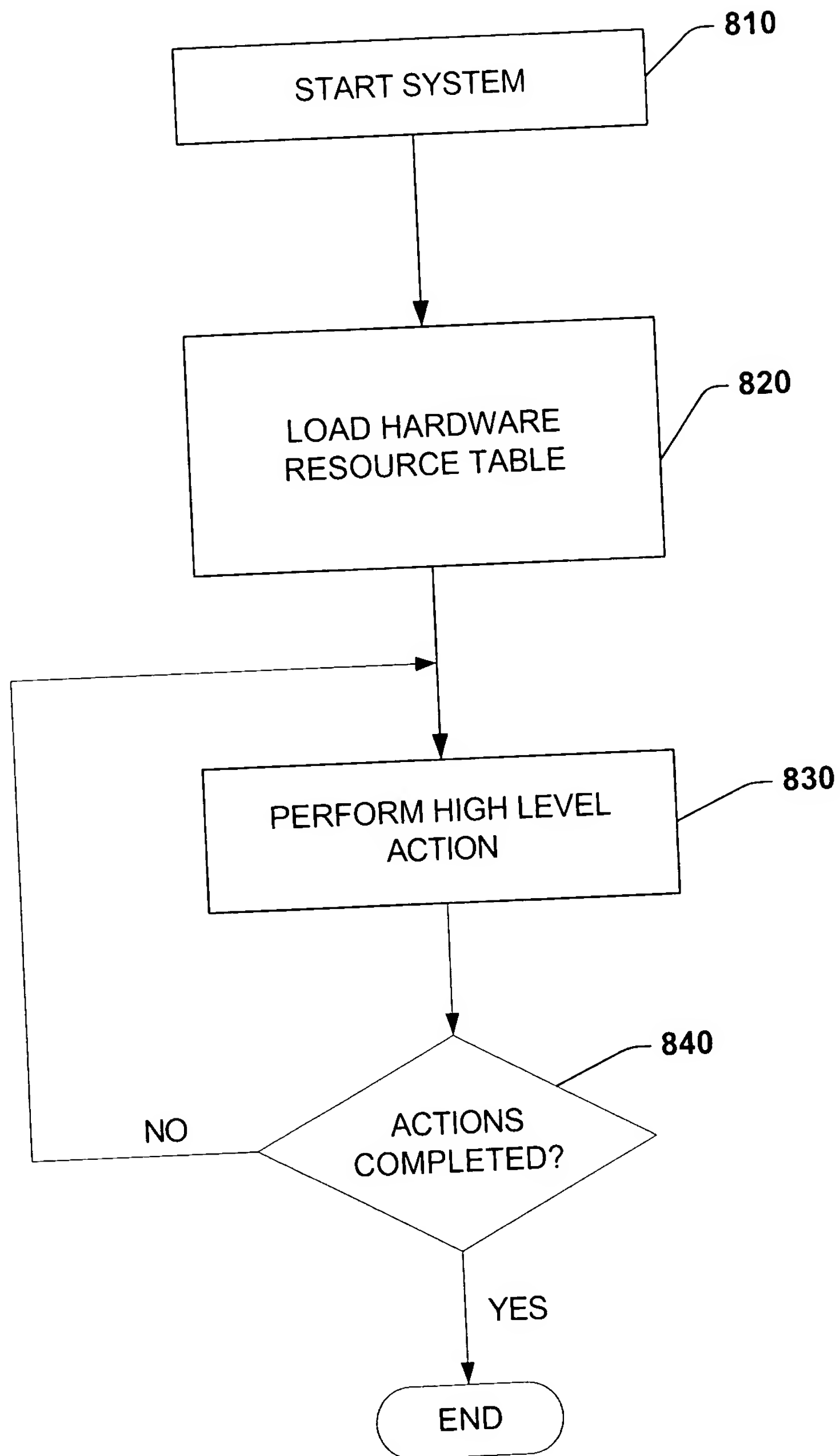
**FIG. 6**



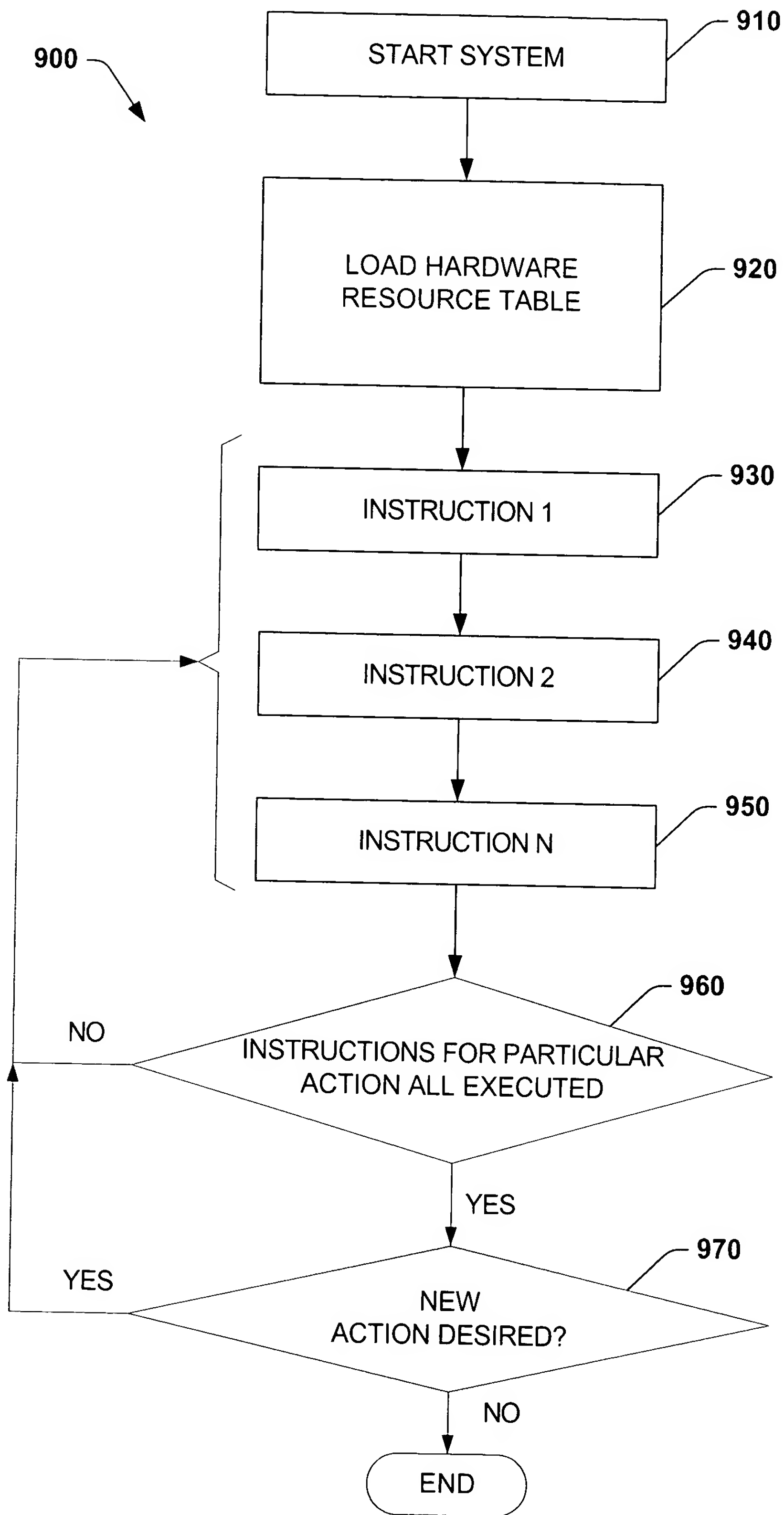
**FIG. 7**



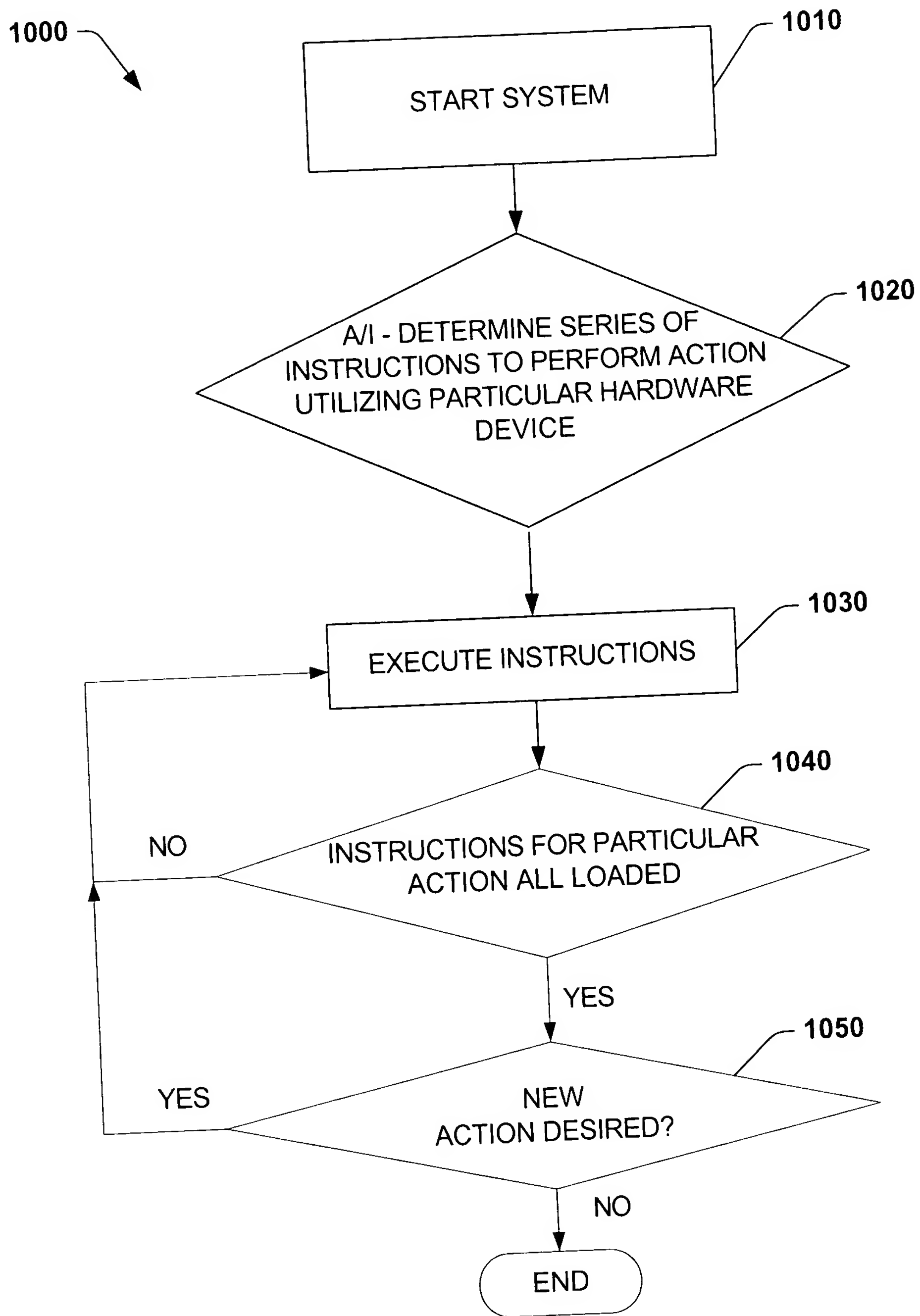
800



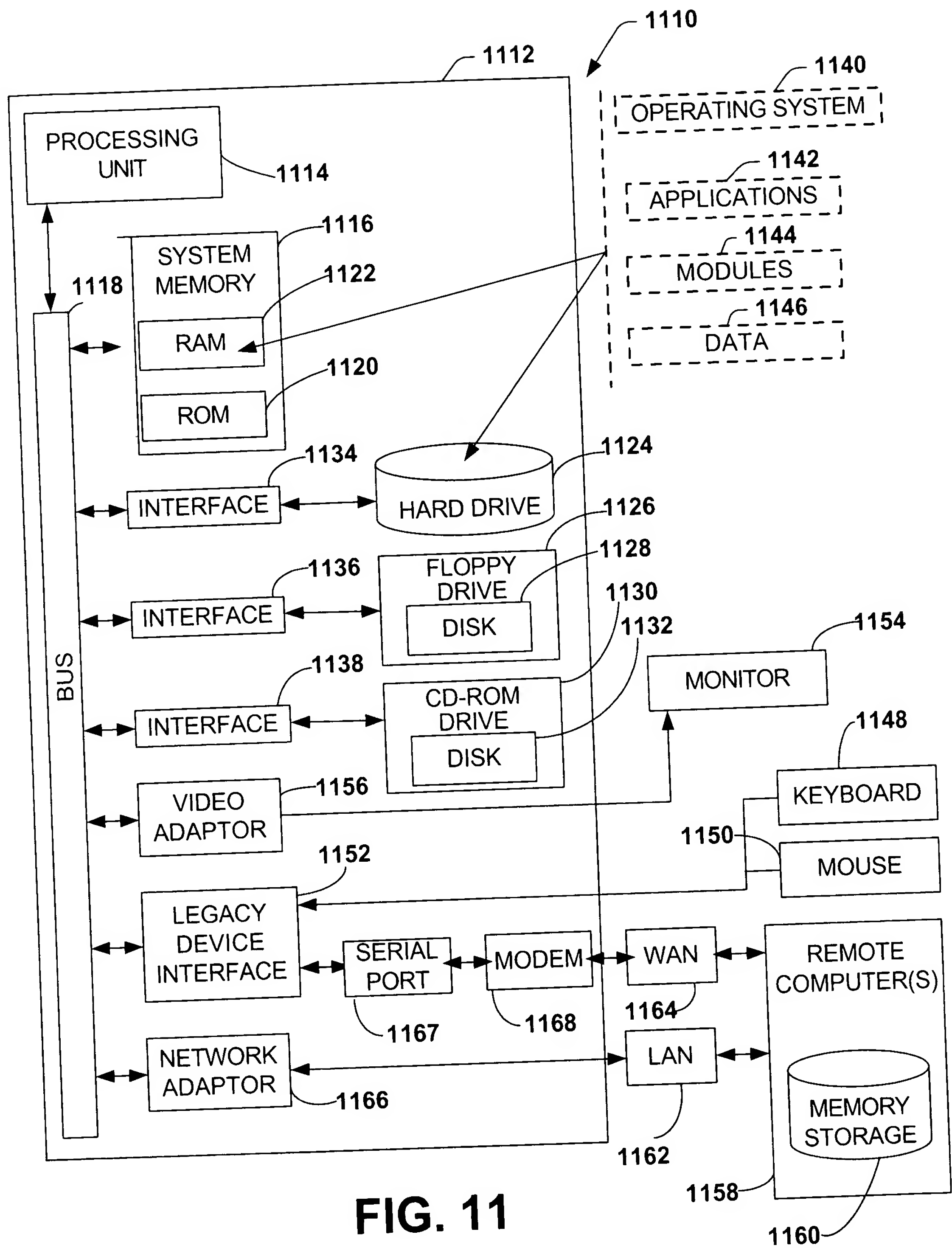
**FIG. 8**



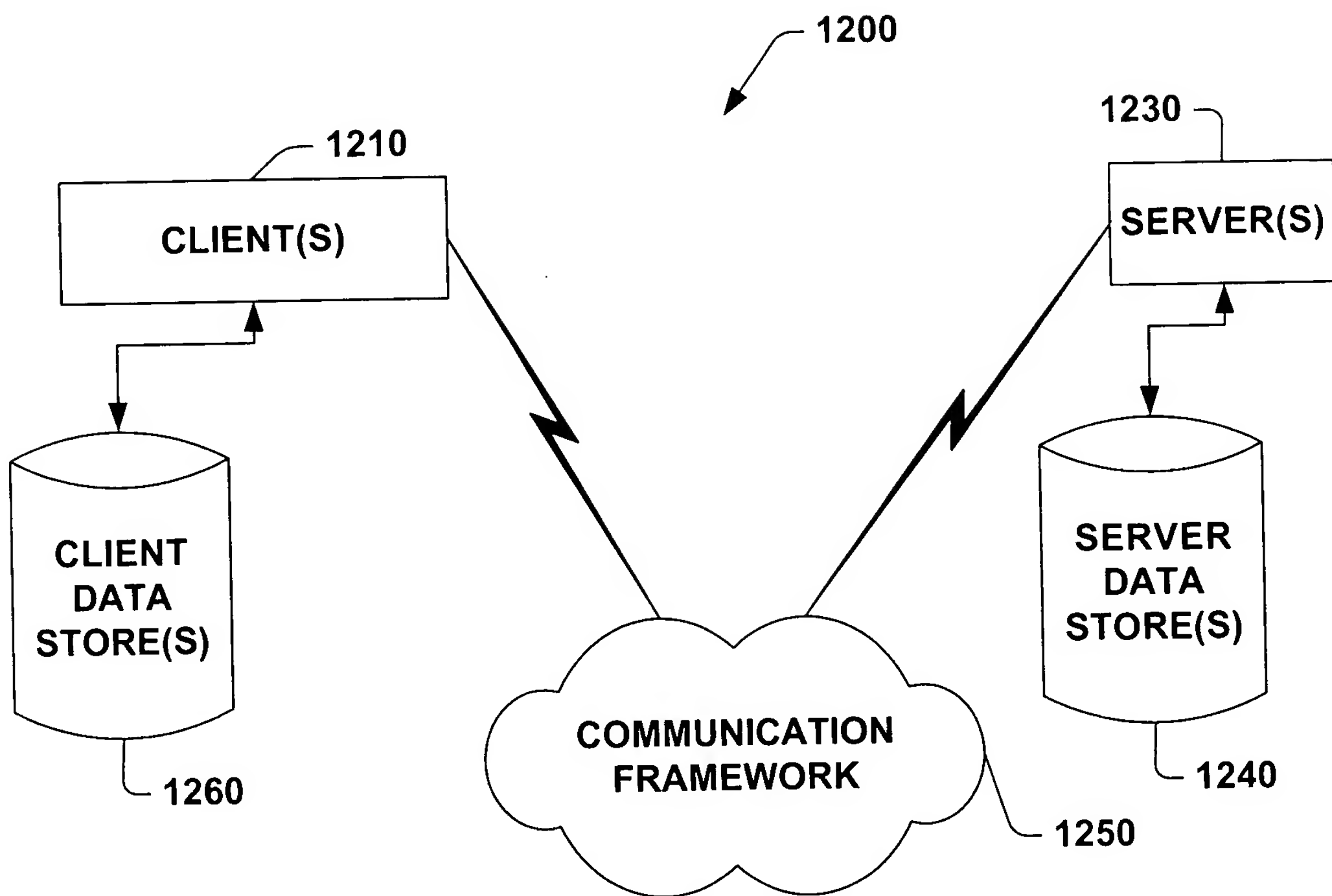
**FIG. 9**



**FIG. 10**



**FIG. 11**



**FIG. 12**